



**P.B.R. VISVODAYA INSTITUTE OF TECHNOLOGY & SCIENCE  
(AUTONOMOUS)**

(Affiliated to J.N.T.U.A, Approved by AICTE and Accredited by NAAC)  
KAVALI – 524201, S.P.S.R Nellore Dist., A.P. India. Ph: 08626-243930



**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**Guest Lecture - REPORT**

<b>DEPARTMENT</b>	: ECE
<b>EVENT</b>	: Guest Lecture
<b>PARTICIPANTS</b>	: IEEE members in II & III B.Tech ECE
<b>TITLE OF PROGRAMME</b>	: <b>Role of IEEE in Engineering Education</b>
<b>RESOURCE PERSON</b>	: <b>Mr. Periasamy Pradeep R,</b> Mentor, IEEE, IEEE-Chennai section, Chennai
<b>DATE</b>	: 28-09-2022.
<b>TIME</b>	: 10:30 A.M. to 12:30 P.M
<b>NAME OF COORDINATOR</b>	: A.Venka Reddy, Asst Professor, Department of ECE.

**OBJECTIVES:**

- To create awareness on Role of IEEE in Engineering Education.

**REPORT:**

The guest lecture was organized by the department of ECE on “**Role of IEEE in Engineering Education**”. In this program 20 students and 03 faculty members were attended. **Mr. Periasamy Pradeep R**, delivered a lecture on “Role of IEEE in Engineering Education”.

**OUTCOMES:**

Students were able to:

- Know their importance of being in professional societies.
- Know what the various professional societies available are.
- Understand how to choose and use the professional societies

**DESCRIPTION OF THE EVENT:**

Resource person **Mr. Periasamy Pradeep R** started Guest Lecture by introducing himself and explain Importance of professional societies. He briefed the benefits of being in the societies and how the professional societies help to know latest happenings and recent developments in the technology.

**FEEDBACK / SUGGESTIONS:**

1. Session was very informative.
2. By attending the Guest lecture, we got to know what is the importance of professional societies.

**PHOTOGRAPHS:**



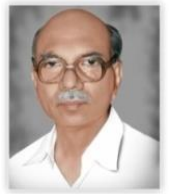


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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**Guest Lecture - REPORT**

<b>DEPARTMENT</b>	: ECE
<b>EVENT</b>	: Guest Lecture
<b>PARTICIPANTS</b>	: IEEE members of II & III B. Tech. ECE Students
<b>TITLE OF PROGRAMME</b>	: <b>VLSI Implementation of Analog circuits</b>
<b>RESOURCE PERSON</b>	: Mr. C. Sai Kumar,  Asst. Professor & IEEE ACM-Hyderabad section,  Dept. of ECE,  VNRVJIET,  Hyderabad, India
<b>DATE</b>	: 11-11-2022
<b>TIME</b>	: 10:30 A.M. to 12:30 P.M
<b>NAME OF COORDINATOR</b>	: A.Venka Reddy, Asst Professor, Department of ECE.
<b>OBJECTIVES:</b>	

- To create awareness on programming styles in VHDL & Verilog.

**REPORT:**

The guest lecture was organized by the department of ECE on “**VLSI Implementation of Analog circuits**”. In this program 30 students and 4 faculty members were attended. Mr. C. Sai Kumar, delivered a lecture and demonstrated the implementation of analog application in VHDL & Verilog.

**OUTCOMES:**

Students were able to

- Know the skill set required for programming VHDL & Verilog
- Know what the various courses that give sound knowledge in VLSI
- Understand the procedure for developing projects in VHDL and Verilog styles

**DESCRIPTION OF THE EVENT:**

Resource person Mr. C. Sai Kumar started Guest Lecture at the right time after his introduction to the audience. He has shown results of their research and explained the logic behind them. He impressed the students by explaining the wonderful packages that are offered for a VLSI engineer if they get the required skillset.



## FEEDBACK / SUGGESTIONS:

1. Session was very informative.
2. By attending the Guest lecture, we got to know what is the importance VLSI for an electronics engineer

## PHOTOGRAPHS:



