



### **VLSI DESIGN LAB:**

The VLSI Design laboratory aims to produce trained professional engineers on par with industry standards and also to significantly contribute to the design aspects of VLSI Design and Device Modelling. The laboratory is equipped with the latest IC design tools and sophisticated hardware facilities which help students to learn the required skills thoroughly. The laboratory caters to the need of the Undergraduate (UG) and Postgraduate (PG) students.

#### **Hardware Facilities:**

- FPGA Kits
- CPLD Kits

#### **PCs Software Tools:**

- Microwind 3.9
- Xilinx 12.0
- Code Composer Studio V6



Figure: VLSI DESIGN LAB



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(AUTONOMOUS)**

*(Affiliated to J.N.T.U.A, Approved by AICTE and Accredited by NAAC)*  
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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**



Figure: FPGA Kits

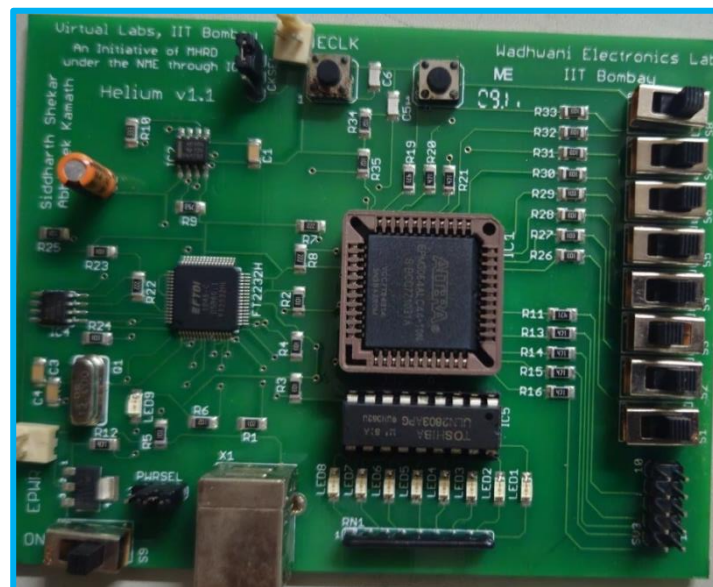


Figure: CPLD Kits