



PARVATHAREDDY BABUL REDDY
VISVODAYA INSTITUTE OF TECHNOLOGY & SCIENCE
(Affiliated to J.N.T.U.A, Approved by AICTE and Accredited by NAAC)
KAVALI-524201, S.P.S.R Nellore Dist., A.P. India. Ph: 08626-243930



DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

LIST OF COMMITTEES & INCHARGES

1. **Department Advisory Committee (DAC)**
2. **Board of Studies Committee (BoS)**
3. **Program Assessment Committee (PAC)**
4. **Departmental Research Committee (DRC)**
5. **Gap Analysis Committee (GAC)**
6. **Course Expert Group (CEG)**



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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

PBRVITS/Admin./EEE/2023-2024/DAC

Date: 01.07.2023

DEPARTMENT ADVISORY COMMITTEE

Objective:

To ensure that the Vision and Mission of the Department are achieved by acting as a catalyst in improving the quality of the programs offered.

This is a core committee of the department constituted to help the decision-making process of the matters pertaining to the department in Academic, Infrastructural facilities, student support systems, short and long term goals.

Composition:

S.No	Member	Name	Designation
1	Head of the Department	Dr. M. V. Ramesh	Chairman
2	NBA Coordinator	Mr. A. Bhakthavachala	Convener
3	Two Expert Members from NITs/Universities	Dr. G. V. Nagesh Kumar Professor and Head, EEE Dept. JNTUA College of Engineering, Pulivendula, A.P. drgvnk14@gmail.com	Members
		Dr. D. M. Vinod Kumar Professor, Department of EEE, NIT, Warangal. Ph. No: 9885294521 vinodkumar.dm@gmail.com	
4	Alumni	MD. Vaseem, General Electric(GE vaseem.0266@gmail.com 9740848738	Members
		Dr. D. M. Vinod Kumar Professor, Department of EEE, NIT, Warangal. Ph. No: 9885294521 vinodkumar.dm@gmail.com	
7	Employer	Mr. U. V. Sai Srinivas Director of Operations Mcore Technologies Pvt Ltd Bangalore, Mob:7624991436 saisrinivas.uv@mcore.in	Member

8	Industry Expert	Mr. S. V. Mahesh Babu, Executive Engineer, APTRANSCO, 400KV Substation, Kalikiri, Chittoor Dt. Mob: 8555029175 sv_maheshbabu@yahoo.com	Member
9	Senior Faculty	Dr. K. Jeyakumar	Member
10	Senior Faculty	Mr. Ch. Srinivasulu Reddy	Member

Meeting Frequency: Twice in an Academic Year.

Tenure: Three Years (Preferably Academic years)

Quorum: 60%

Roles and Responsibilities: The roles and responsibilities of the DAC are as follows:

1. Suggestion on the Academic Infrastructure, Budget and ICT facilities
2. To suggest improvements in academic plans and recommend standard practices/systems for attainment of PEOs
3. To encourage Industry-Institute interactions to bridge the gaps
4. To suggest quality improvement initiatives to enhance employability of students.
5. To monitor and advice on current and future issues related to the programme
6. To define, review and redefine OEOs to identify and suggest thrust areas to conduct various activities (final year projects, training courses and additional experiments) to meet PEO's
7. To ensure the quality in teaching learning process through Faculty Development Programs
8. To identify latest domains to organize FDPs
9. To review survey reports, analyze feedbacks and suggest an action plan
10. To review reports submitted by PAAC and monitor the progress of the program

Deliverables: To submit reports to IQAC on effectiveness of program implementation in terms of program.



Principal
Principal

PARVATHAREDDY BABUL REDDY
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KAVALI - 524201, S P S R, Nellore Dist. A

Date: 05.07.2023



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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

PBRVITS/Admin./EEE/2023-2024/BOS

Date: 01.07.2023

BOARD OF STUDIES COMMITTEE

Objective:

The Board of Studies (**BoS**) is the basic constituent of the academic system of an Institute. Its functions will include framing the content of various courses, reviewing and updating the content from time to time, introducing new courses of study etc.

Composition:

S.No	Member	Name	Designation
1	Head of the Department	Dr. V. Ramesh	Chairman
2	Expert Member from NIT	Dr. D. M. Vinod Kumar Professor, Department of EEE, NIT, Warangal. Ph. No :9885294521 vinodkumar.dm@gmail.com	Member
3	Expert Member from IISc	Dr. G. Gurunath Associate Professor Department of EEE IISc, Bangalore Phone: 8022933556 gurunath@iisc.ac.in	Member
4	Expert Member from University	Dr. Ch. Chengaiah Professor and Head, Dean SCDC Department of EEE, SVU, Thirupathi Phone: 9440403023 chinthapudisvu@gmail.com	Member
5	Expert member from Industry	Mr. S. Venkata Mahesh Babu Executive Engineer, 400KV Substation AP TRANSCO, Kalikiri, Chittoor Ph. No : 8555029175 sv_maheshbabu@yahoo.com	Member

6	Alumni	Mrs. Vemuri Supriya Asst. Executive Engineer APSPDCL, Nellore, AP Ph. No : 9849952122 vemurisupriya123@gmail.com	Member
7	Senior Faculty	Dr. K. Jeyakumar	Member
8	Internal Faculty	Mr. A. Bhakthavachala Mr. Ch. Srinivasulu Reddy Mr. V. Masthanaiah Mr. G. Suman Mr. S. Muni Raja	Members

The composition of the Board of Studies is given below:

- The Head of the Department as Chairman.
- All Professor/Associate Professor/Assistant Professor of the Department.
- One senior professor to be co-opted by the BoS from other department nominated by the Director in consultation with Head of Department
- One subject expert (**ACADEMIC**) nominated by the Director on the recommendation of the Head of concerned Department.
- v. Experts should be from (i) IISc / IITs / NITs / other reputed academic Institutions (Government / Aided)
- vi. One subject expert (**R&D / INDUSTRY**) nominated by the Director on the recommendation of the Head of concerned Department.

Meeting Frequency: Once in an Academic Year.

Tenure: Three years (Preferably Academic years)

Quorum: 60%

Note: Any of the Directors and Joint Directors may be called to the meeting as special invitee whenever required.

Roles and Responsibilities: The roles and responsibilities of the BOS are as follows:

1. The Board of Studies of a department in the college shall:

- prepare syllabi for various courses keeping in view the objectives of the college interest of the stakeholders and national requirement for consideration and approval of the Academic Council;
- Suggest methodologies for innovative teaching and evaluation techniques;
- Suggest panel of names to the Academic Council for appointment of examiners; and
- Coordinate research, teaching, extension and other academic activities in the department/college.

Date: 05.07.2023


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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

PBRVITS/Admin./EEE/2023-2024/PAC

Date: 01.07.2023

PROGRAM ASSESSMENT COMMITTEE(PAC)

Objective:

To enhance the quality of program and to ensure continuous improvement in terms of curriculum, teaching learning processes, Student performance, Faculty contributions, infrastructural facilities, student support systems and industry interaction by effectively involving stakeholders.

Composition:

Program Assessment Committee (PAC) Members:

S.No	Member	Name	Designation
1	Head of the Department	Dr. M. V. Ramesh ^{Professor} Mobile No: 9281461245 Email-Id: Ramesh.mv@visvodayata.ac.in	Chairman
2	NBA Coordinator	Mr. A.Bhakhavachala ^{Assoc. Professor} Mobile No: 9848651775 Email-Id: bhakhavatsala.a@visvodayata.ac.in	Convener
3	Senior Faculty	Mr. Ch Srinivasu Reddy ^{Assoc. Professor} Mobile No: 9618158057 Email-Id: srinivasulureddy.ch@visvodayata.ac.in	Member
4	Senior Faculty	Mr. V. Masthanaiah ^{Asst. Professor} Mobile No: 9052183686 Email-Id: masthanaiah.v@visvodayata.ac.in	Member
5	Senior Faculty	Mr. G. Suman Mobile No: 9441827001 Email-Id: suman.g@visvodayata.ac.in	Member

Note:Any of the Directors and Joint Directors may be called to the meeting as special invitee whenever required.

Meeting Frequency: Twice in an Academic Year.

Tenure: Five Years (Preferably Academic years)

Quorum: 60%

Roles and Responsibilities: The roles and responsibilities of the PAC are as follows:

1. To develop, implement and monitor quality parameters for various Academic and administrative activities within the department.
2. To create a learner centric environment
3. To collect feedback from stakeholders, take necessary actions and measuring the effectiveness.
4. To compute and continuously monitor attainment of PO'S and PSO'S
5. To evaluate programme effectiveness and take necessary steps for continuous improvement.
6. To motivate faculty and students to attend workshops, developing projects, working models, paper publications and research.
7. To interact with students, faculty, CEG's, program coordinator and external stakeholders to understand the effectiveness of program in order to achieve the PEO's
8. To review reports submitted by CEG's, initiate remedial measures, facilitate academic audit and prepare an action plan for improvement.
9. To prepare an action plan to enhance research activities in coordination with the departmental research committee (DRC) review survey reports, analyze feedbacks and suggest an action plan
10. To set targets for PO attainment.

Deliverables:

Submit reports to DAC on effectiveness of program implementation in terms of

1. Curriculum- Gaps identified, Action taken, Impact
2. Stakeholder's survey- Analysis, Action taken, Impact
3. Faculty contribution- Analysis, Action taken, Impact
4. Infrastructural facilities- Analysis, Action taken, Impact
5. Student support systems- Analysis, Action taken, Impact
6. Industry interaction- Analysis, Action taken, Impact
7. Attainment of PO'S and PSO'S- Gaps identified, Action taken, Impact

Date: 05.07.2023


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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

PBRVITS/Admin./EEE/2023-2024/DRC

Date: 01.07.2023

DEPARTMENTAL RESEARCH COMMITTEE

Objective of DRC:


In order to streamline the research activities and increase the productivity in terms of projects, Publications and also improve quality of training programmes, a dedicated 'Departmental Research Committee' with the following members is constituted for EEE department.

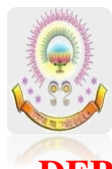
Composition:

S.No	Member	Name	Designation
1	Head of the Department	Dr. V. Ramesh	Chairman
2	Professor-EEE-PBR VITS	Dr. C. Raja Selvam	Member
3	Professor-EEE-PBR VITS	Dr. K. Jeyakumar	Member
4	Industry Expert	Mr U V Sai Srinivas Director of Operations Emcore Technologies Pvt Ltd Bangalore, Mob:7624991436	Member
5	Expert Member from JNTUA	Dr. G. V. Nagesh Kumar Professor and Head, EEE Dept. JNTUA College of Engineering, Pulivendula. A.P.	Member
6	NBA Coordinates UG and PG	Mr. A. Bhakthavachala	Convener
7	Senior Faculty	Mr. CH. Srinivasulu Reddy	Member

The term of the membership of all the members of DRC shall be for a period of two years. Unless otherwise specified minimum three members of the DRC shall form the quorum. The meeting of the DRC still is held at least once every six months. It may be held frequently depending upon the necessity.

Date: 05.07.2023


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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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PBRVITS/Admin./EEE/2023-2024/GAP

Date: 15-06-2023

DEPARTMENTAL COMMITTEES' CONSTITUTION AND RESPONSIBILITIES

GAP ANALYSIS COMMITTEE (GAC)

Objective:

The main objective of the gap analysis is to assess the gap between a student's learning attainments in the existing curriculum and the requirements of the current trend in the present world. And also to bridge the gap between academic curriculum and various industries need so that the student has the required knowledge.

Composition:

S.No	GAC member	Designation & Company	Position
1	Dr. M. V. Ramesh	Head of the department PBR VITS, Kavali SPSR Nellore, A.P-524201	Convener
2	Mr. S. V. Mahesh Babu	Executive Engineer, 400KV Substation AP TRANSCO, Kalikiri, Chittoor, AP-517234 Mob: 8555029175 sv_maheshbabu@yahoo.com	Industry Member
3	Dr. V. Raveendra Reddy,	Professor of EEE RSR Engineering College Kadanuthala, Kavali SPSR Nellore, A.P-524201 Mob: 7981303284	Alumni Member
4	Mr. Ch. Srinivasulu Reddy	Assistant Professor PBR VITS, Kavali SPSR Nellore, A.P-524201	Senior Faculty Member
5	Mr. A. BhakthaVachala	Assistant Professor PBR VITS, Kavali SPSR Nellore, A.P-524201	Senior Faculty Member
6	Mr. V. Masthanaiah	Assistant Professor PBR VITS, Kavali SPSR Nellore, A.P-524201	Senior Faculty Member
7	Mr. G. Suman	Assistant Professor PBR VITS, Kavali SPSR Nellore, A.P-524201	Senior Faculty Member
8	Mr. S. Muni Raja	Assistant Professor PBR VITS, Kavali SPSR Nellore, A.P-524201	Senior Faculty Member

Meeting Frequency: Twice in an Academic Year.

Tenure: Three Years (Preferably Academic Years)

Quorum: 60%

Roles and Responsibilities: The roles and responsibilities of the GAP as follows:

1. To conduct a meeting twice a year or semester once.
2. Collect the gap in the each and every subject from the respective faculty members.
3. Use the following process to identify the gaps in the curriculum
 - a. Mapping of Curriculum with POs and PSOs
 - b. Guidance from Department Advisory Board
 - c. Collect Syllabus Feedback from Stakeholders
4. After identifying the Gaps the following action is to taken to bridge the GAP

Convey the identified gaps to Board of Studies: Considering the feedback from faculty, PAC committee, students and DAC committee, a representation is prepared by the department to convey the gaps and possible action plan to the Board of Studies (BOS) of University. These inputs are taken into consideration by BOS while revising the syllabus.

Deliverables: To submit reports to DAC on effectiveness of program GAPs finding in the current curriculum of program.

Date: 20.06.2023


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PBRVITS/Admin./EEE/2023-2024/CEG

Date: 16.07.2023

COURSE EXPERT GROUP(CEG)

Objective: The course expert team is responsible in exercising the **central domain of expertise** in developing and renewing the curriculum and assessing its quality and effectiveness to the highest of professional standards.

Composition:

Course Expert Group (CEG) Members:

S. No	Course Specializations	Chairperson of Course Expert	Designation
1	Electrical circuits	Dr. M. V. Ramesh <small>Professor</small> Mob:9281461245 Ramesh.mv@visvodayata.ac.in	HOD-EEE
2	Electrical machines	Mr. A. Bhakthavatsala <small>Assoc. Professor</small> Mob: 9848651775 bhakthavatsala.a@visvodayata.ac.in	Senior Faculty
3	Power systems	Mr. Ch. Srinivasulu Reddy <small>Assoc. Professor</small> Mob:9618158057 srinivasulureddy.ch@visvodayata.ac.in	Senior Faculty
4	Electronics Engineering	Mr.G.Suman <small>Asst. Professor</small> Mob: 9441827001 suman.g@visvodayata.ac.in	Member
5	Power Electronics	Mr.Y.Davidu <small>Asst. Professor</small> Mob:8106597968 David.y@visvodayata.ac.in	Member
6	Controls and Measurements	Mr. V. Masthanaiah <small>Asst. Professor</small> Mob: 9052183686 masthanaiah.v@visvodayata.ac.in	Member

➤ **Roles and Responsibilities:** The roles and responsibilities of the PAC are as follows:

- Curriculum, including establishing prerequisites.

- Inform the Academic Committee the 'day-to-day' matters as are relevant to the offered courses.
- Consider student and staff feedback on the efficient and effective development of the relevant courses.
- Review the following: Course Descriptions (Prerequisites, Course Objectives, Course Outcomes, Course Outcomes mapping to Program Outcomes / Program Specific Outcomes, Articulation Matrix); Delivery of Modules; Module Outlines; Module Assessment Tasks; Development of Course Full Stack; Review Course Outcomes and Program Outcomes / Program Specific Outcomes Assessment reports.



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Date: 20.07.2023